

# **Notice of References Cited**

Application/Control No.  
09/651,425

Applicant(s)/Patent Under  
Reexamination  
SONGER ET AL.

Examiner  
Tuan A Vu

Art Unit  
2124

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-5,535,406	07-1996	Kolchinsky, Alexander	712/10
	C	US-6,457,116	09-2002	Mirsky et al.	712/16
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	E	US-6,263,302	07-2001	Hellestrand et al.	703/17
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## **NON-PATENT DOCUMENTS**

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	U	"Efficient and Flexible Cosimulation Environment for DSP Applications", Wongyong Sung, Soonhoi Ha; Dec. 1998, IEICE Trans. Fundamentals, Vol. E81-A, No. 12, pp. 2605--2611
	V	"An Integrated Hardware-Software Cosimulation Environment for Heterogeneous Systems Prototyping", Yongjoo Kim et al., June 1996, In Proc. of Seventh IEEE International Workshop on Rapid System Prototyping, pages 66-71
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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